

Appln No. 09/550,757

Amdt date April 27, 2004

Reply to Office action of January 14, 2004

Amendments to the Specification:

Please replace the paragraph beginning on page 33, line 28 with the following rewritten paragraph:

C) With particular reference to FIG. 7, cooperation of the coefficient ramping circuit 604 with the Tomlinson-Harashima precoder 603, both also of FIG 6, is shown. Cooperation of the coefficient ramping circuit 504 with the Tomlinson-Harashima precoder 503, both of FIG. 5, is similar, with the outputs 79a-79b 79d of the coefficient ramping circuit 604 being communicated via the channel 62, rather than being communicated solely within the transmitting transceiver 601. As those skilled in the art will appreciate, Tomlinson-Harashima precoder 603 includes a plurality of delay registers 71a-71d, the output of each of which is modified by taps 73a-73d. Taps 73a-73d contain coefficients $p_1 - p_N$, respectively. The outputs of taps 73a-73d are summed by adders 75a-75c and the outputs of adders 75a-75c are combined with the information to be transmitted via adder 602. Mod2 77 truncates or rounds the output of adder 602 to provide the desired number of bits b_n for input to transmitter 61 (FIG. 6).